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**\*\* CONTINUING DATA \*\*\*\*\***  
 This application is a 371 of PCT/JP04/15835 10/26/2004 *SPS*

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***  
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**\*\* IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\***  
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Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance	<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWINGS</b> 27	<b>TOTAL CLAIMS</b> 1	<b>INDEPENDENT CLAIMS</b> 1
35 USC 119(a-d) conditions met Verified and Acknowledged	<i>[Signature]</i> Examiner's Signature	Initials				

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**TITLE**  
 MANUFACTURING METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

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